
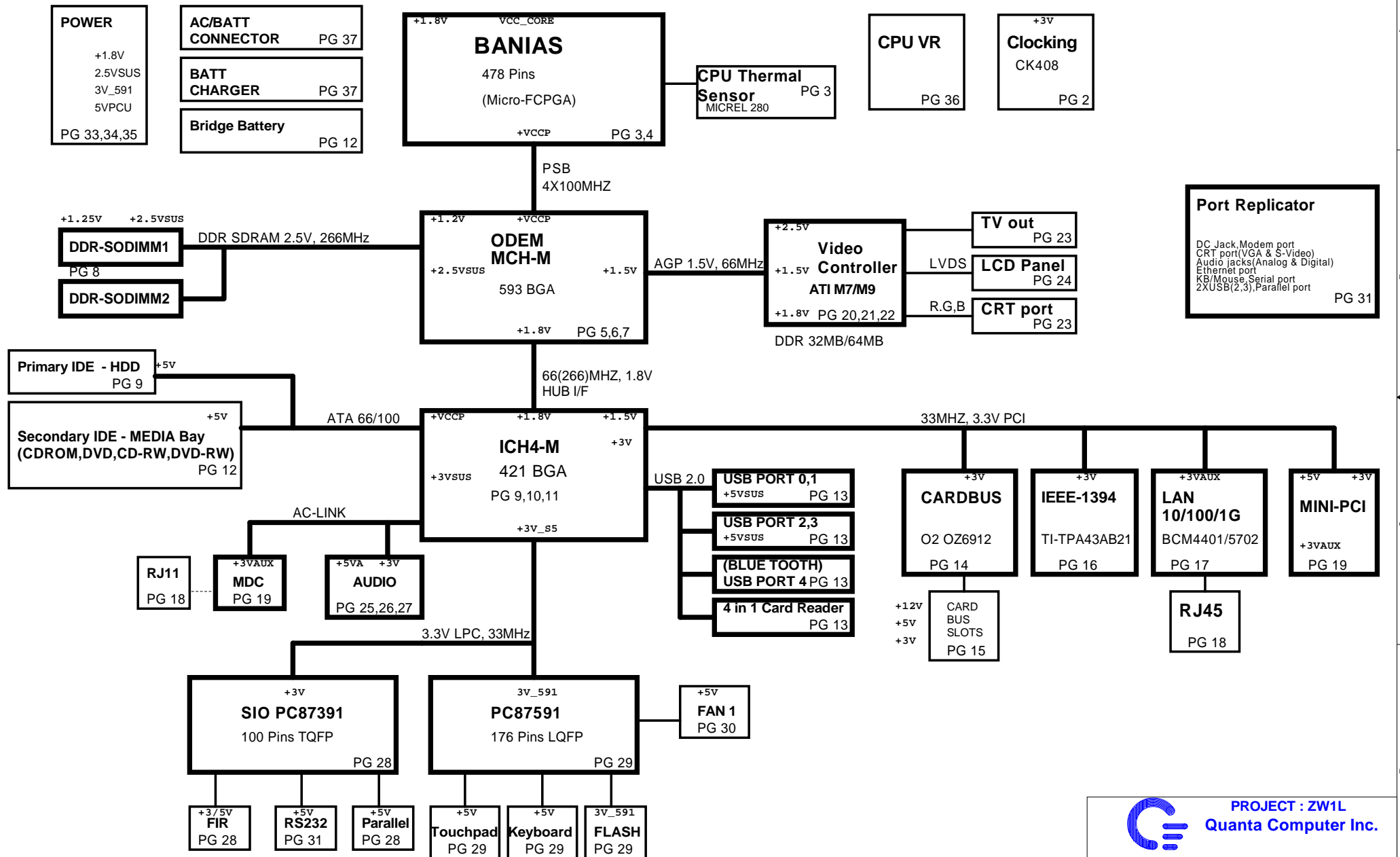


Model	REV	CHANGE LIST	MODEL		
			Page	From	To
ZW1L MB	3A	FIRST RELEASED.	1	3A	
	3B	Reference ECN E200305-3099	2	3A	
			3	3A	
	3C	Reference ECN E200306-2080	4	3A	
			5	3A	
			6	3A	
			7	3A	
			8	3A	
			9	3A	
			10	3A	
			11	3A	
			12	3A	
			13	3A	3C
			14	3A	
			15	3A	
			16	3A	
			17	3A	
			18	3A	
			19	3A	
			20	3A	
			21	3A	
			22	3A	
			23	3A	
			24	3A	
			25	3A	
			26	3A	
			27	3B	
			28	3A	
			29	3A	
			30	3A	
			31	3A	
			32	3A	
			33	3A	
			34	3A	
			35	3A	
	36	3A			
	37	3A			
	38	3A	3C		

 PROJECT : ZW1L Quanta Computer Inc.	DOC NO.	PROJECT MODEL :	ZW1L	APPROVED BY:	Steven Hsu	DATE:	2003,07,29
	204	PART NUMBER:	31ZW1MB0012	DRAWING BY:	Andy Liu	REVISION:	3C

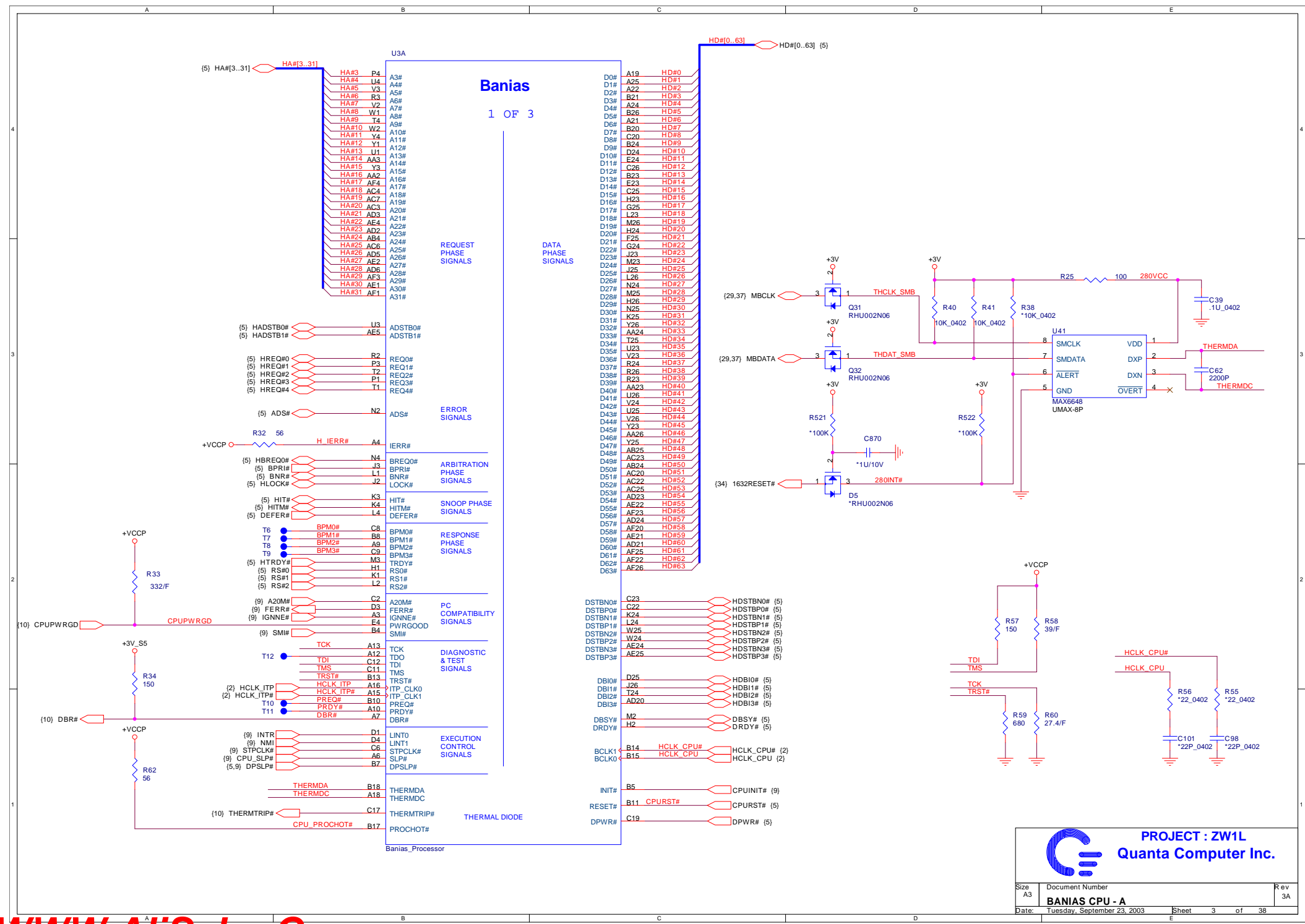
ZW1L BLOCK DIAGRAM BANIAS / ODEM




PROJECT : ZW1L
Quanta Computer Inc.

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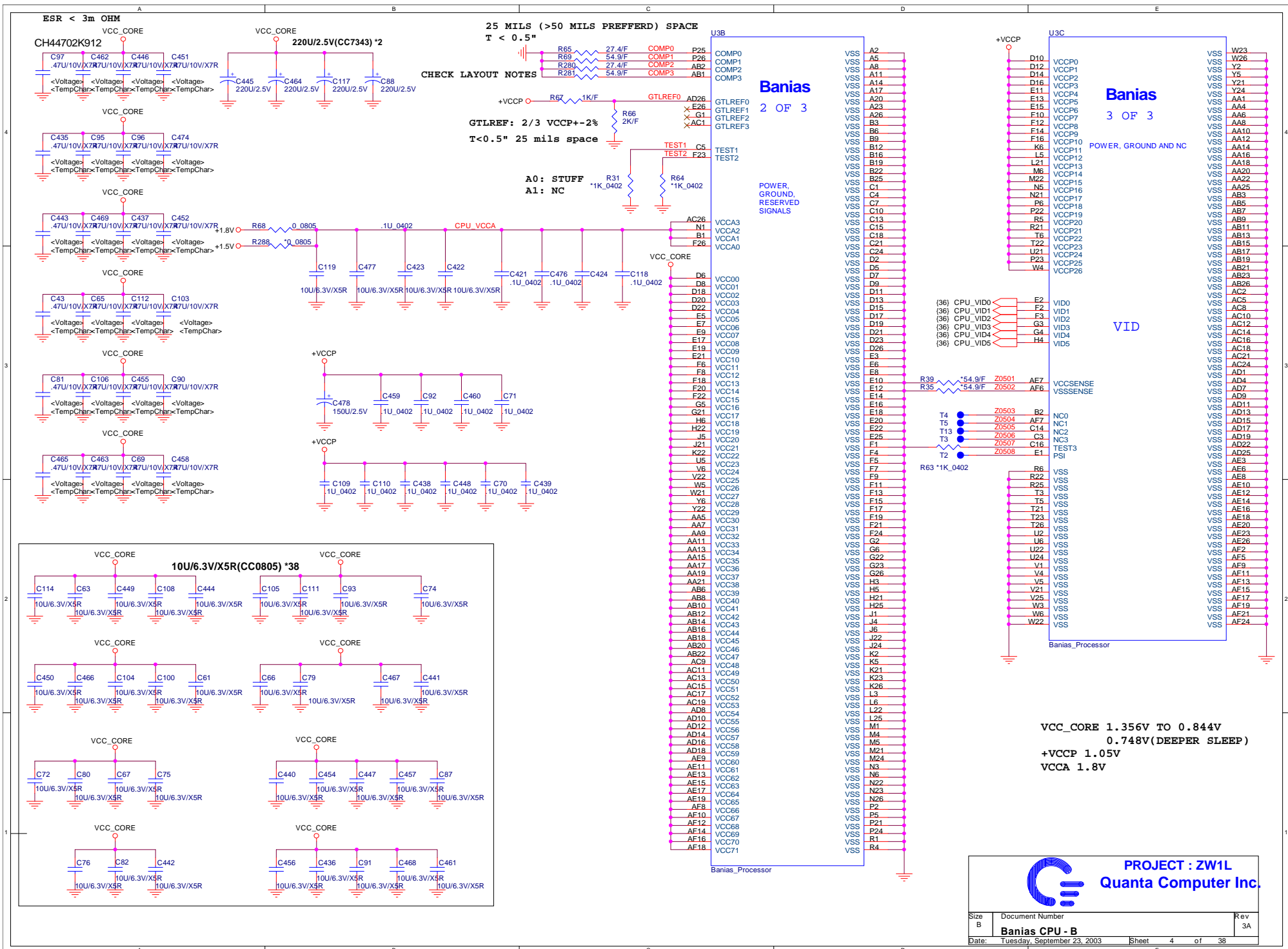


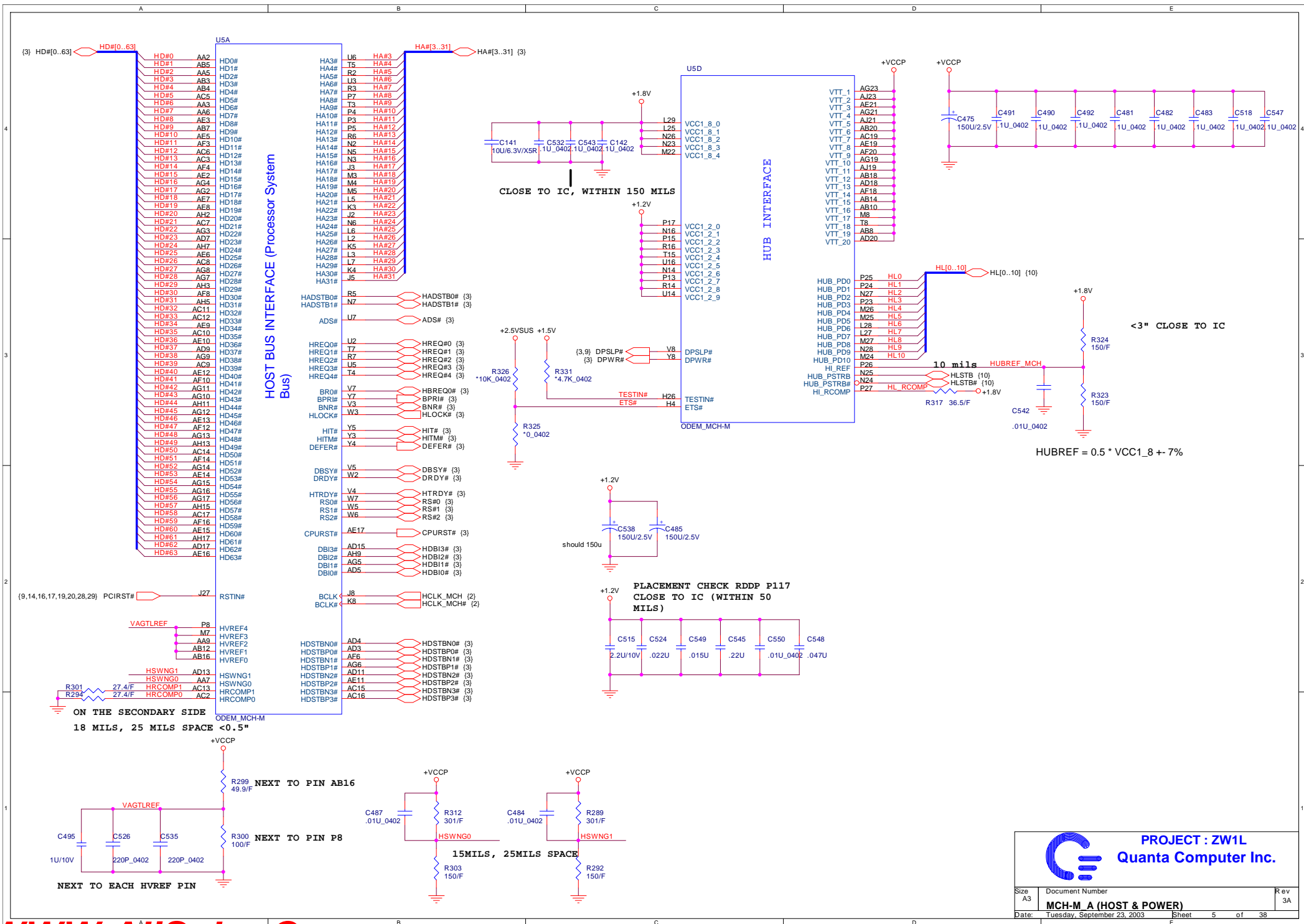


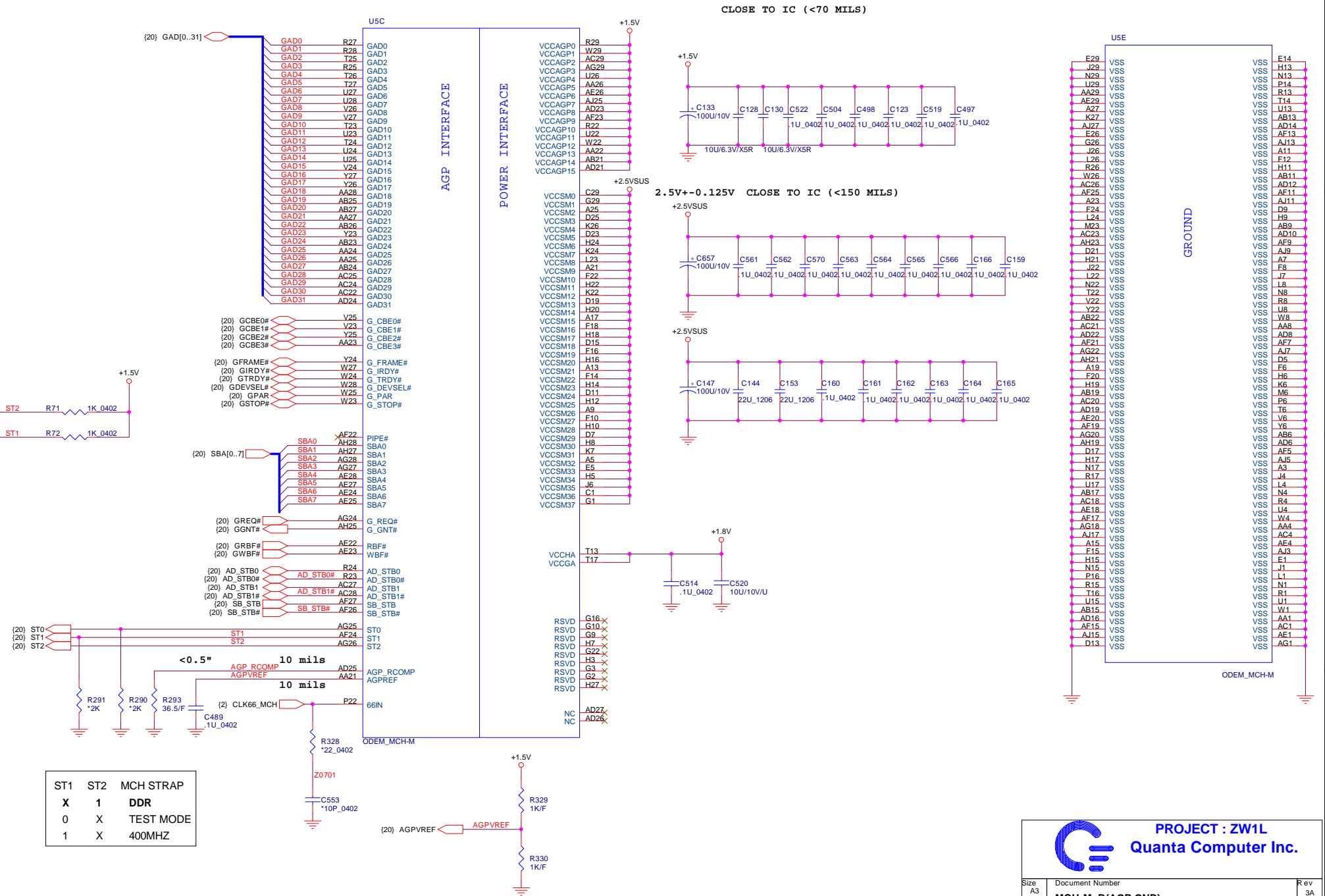
PROJECT : ZW1L

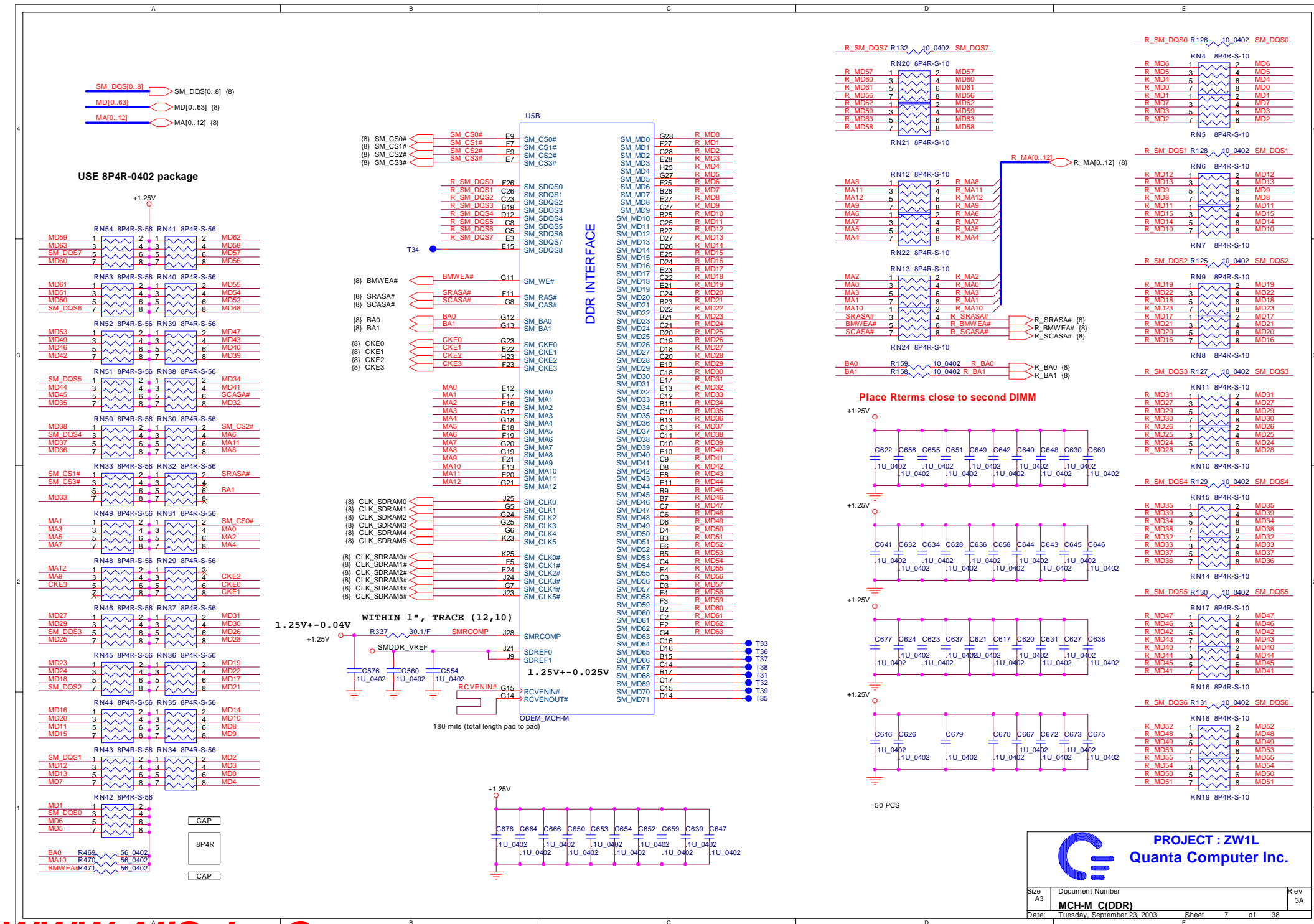
Quanta Computer Inc.

Size A3	Document Number BANIAS CPU - A	Rev 3A
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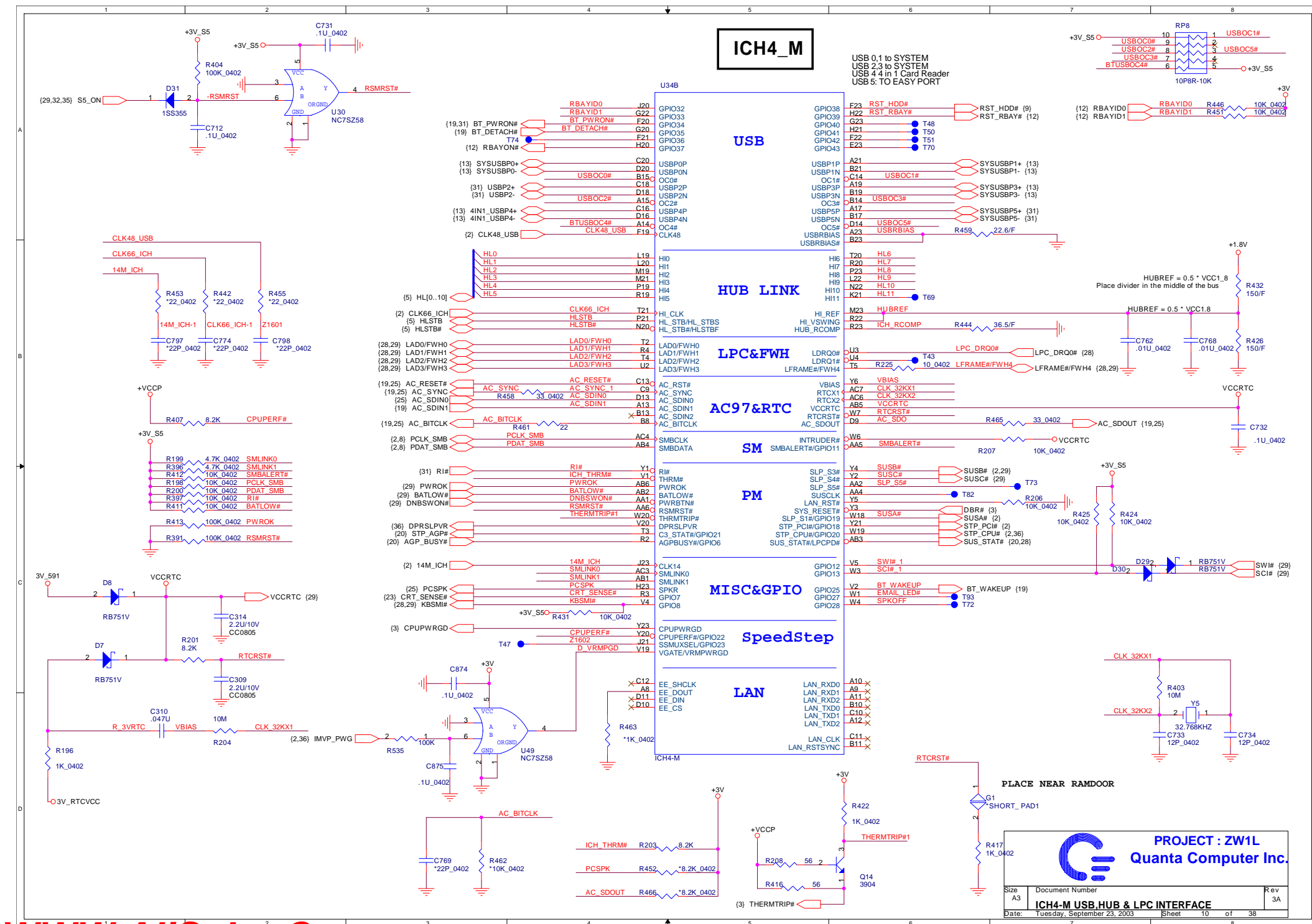


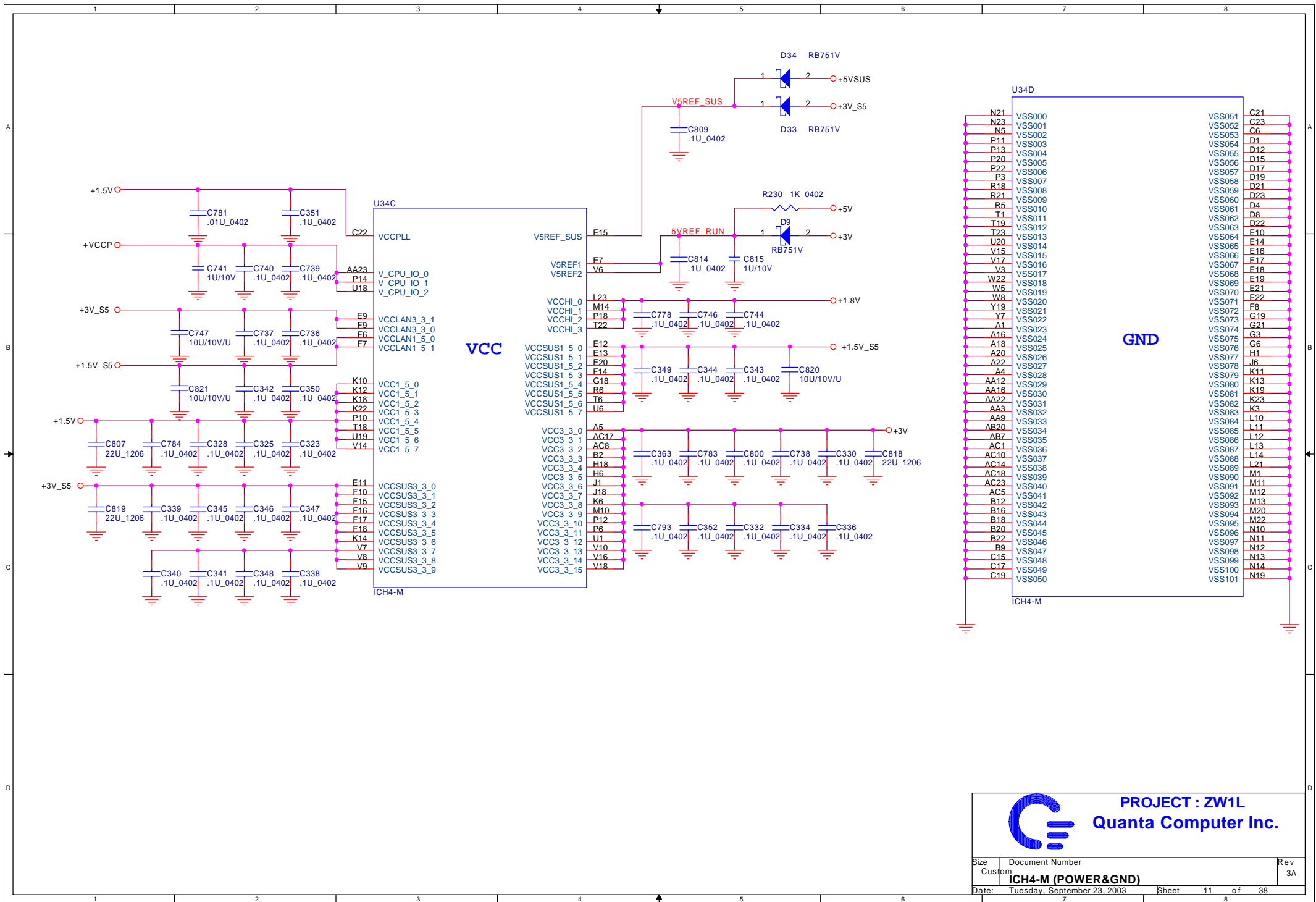


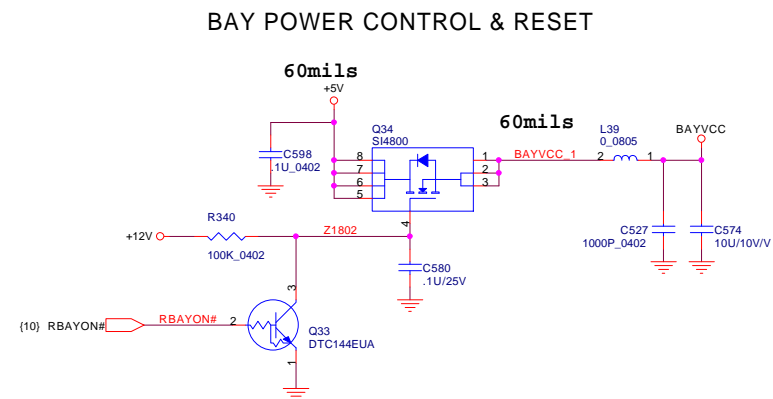
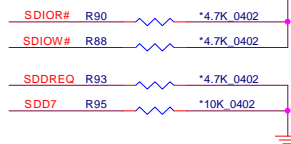
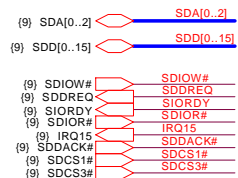




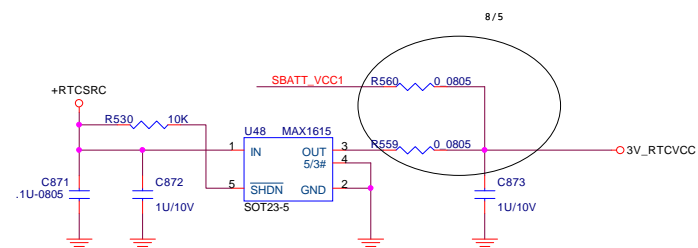






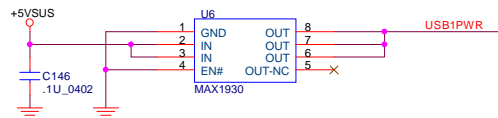


RBAYID0	RBAYID1	STATUS
0	0	FDD
0	1	HDD
1	0	CD/DVD

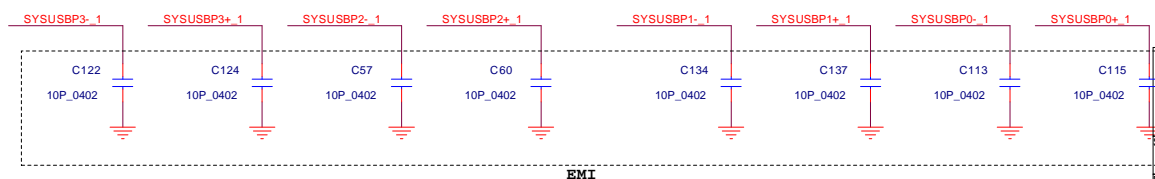
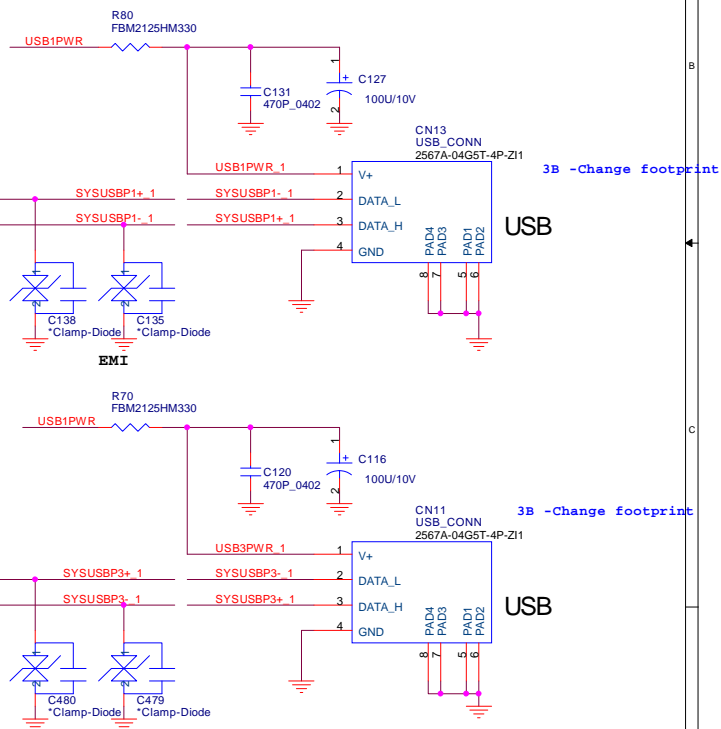
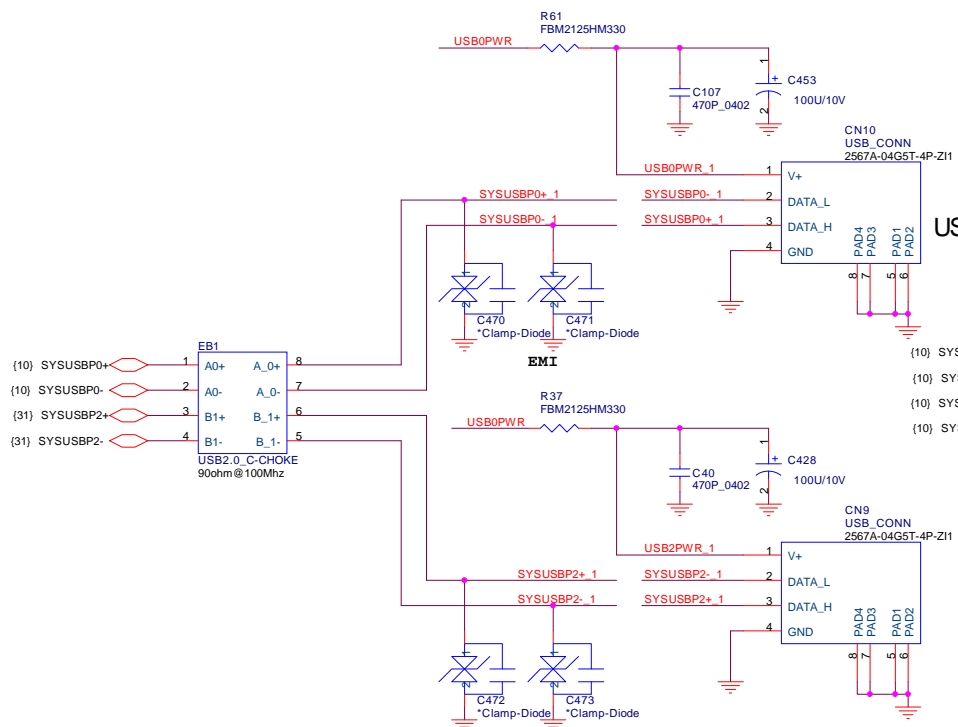
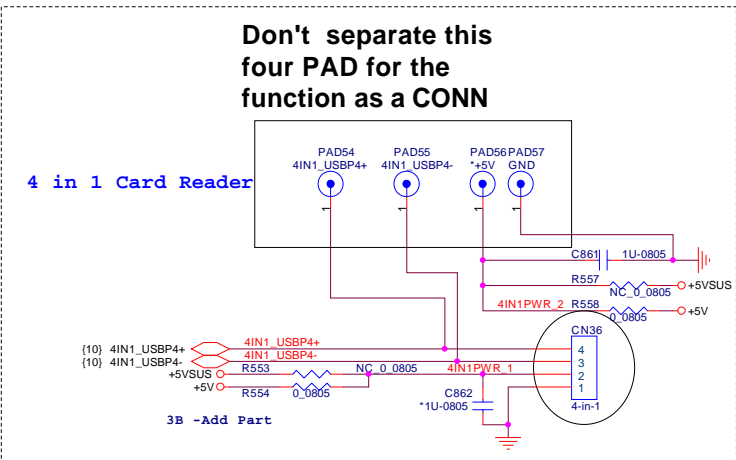
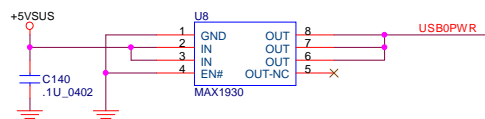


BATTERY SELECT STATUS		
7.2V	3V	STATUS
V	X	PR175
V	X	R559
X	V	R560

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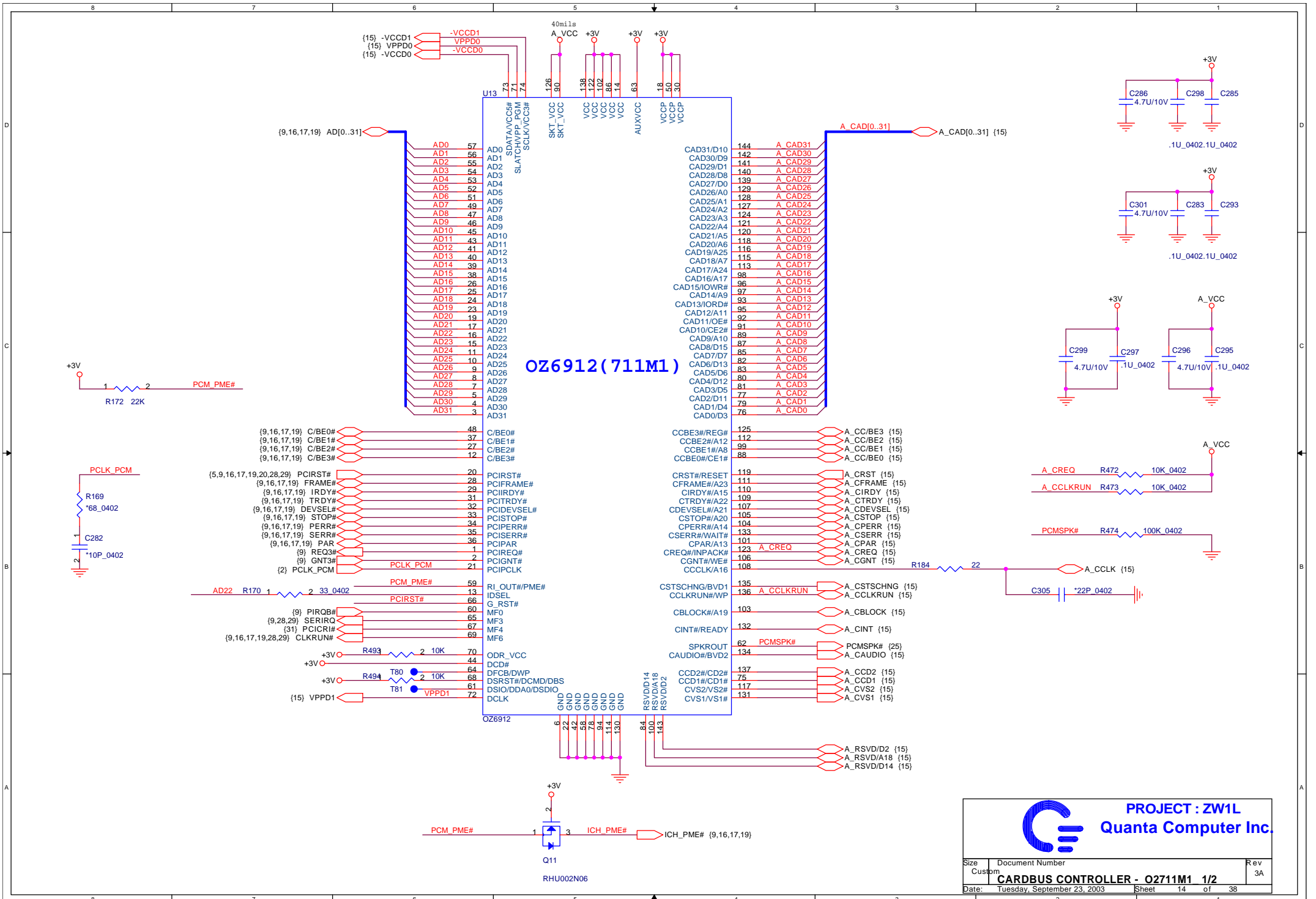


Need key in
MAX1930 S/N



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Quanta Computer Inc.

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CARDBUS slot

CARDBUS PCMCIA SOCKET

3A -CHANGE CARDBUS CON

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Quanta Computer Inc.

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Diagram illustrating the wiring connections for the CARDBUS PCMCIA SOCKET. The connections are organized into four main sections, each corresponding to a 16-pin segment of the connector (CON1).

Section 1 (Pins 1-17):

- 1: GND
- 2: D3 - CAD0
- 3: D4 - CAD1
- 4: D5 - CAD3
- 5: D6 - CAD5
- 6: D7 - CAD7
- 7: CE1 - CCBE0
- 8: A10 - CAD9
- 9: OE - CAD11
- 10: A11 - CAD12
- 11: A9 - CAD14
- 12: A8 - CCBE1
- 13: A13 - CPAR
- 14: A14 - CPERR
- 15: WE/PGM - CGNT
- 16: RDY/BSY, I/O*INT
- 17: VCC

Section 2 (Pins 18-34):

- 18: VPP1
- 19: A16 - CCLK
- 20: A15 - CIRDY
- 21: A12 - CCBE2
- 22: A7 - CAD18
- 23: A6 - CAD20
- 24: A5 - CAD21
- 25: A4 - CAD22
- 26: A3 - CAD23
- 27: A2 - CAD24
- 28: A1 - CAD25
- 29: A0 - CAD26
- 30: D0 - CAD27
- 31: D1 - CAD29
- 32: D2 - RFU
- 33: WP, IOIS16-CKRUN
- 34: GND

Section 3 (Pins 35-51):

- 35: GND
- 36: CD1 - CCD1
- 37: D11 - CAD2
- 38: D12 - CAD4
- 39: D13 - CAD6
- 40: D14 - RFU
- 41: D15 - CAD8
- 42: CE2 - CAD10
- 43: RFSH, VS*1-CVS1
- 44: IORD-CAD13
- 45: IOWR-CAD15
- 46: A17 - CAD16
- 47: A18 - RFU
- 48: A19 - CBLOCK
- 49: A20 - CSTOP
- 50: A21 - CDEVSEL
- 51: VCC

Section 4 (Pins 52-68):

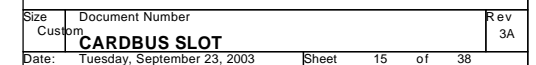
- 52: VPP2
- 53: A22 - CTRDY
- 54: A23 - CFRAME
- 55: A24 - CAD17
- 56: A25 - CAD19
- 57: NC - CVS2
- 58: RESET-CRST
- 59: WAIT-CSERR
- 60: INPACK-CREQ
- 61: REG - CCBE3
- 62: BVD2, SP-CAUDIO
- 63: BVD1, STSCHG-C*
- 64: D8 - CAD28
- 65: D9 - CAD30
- 66: D10 - CAD31
- 67: CD2 - CCD2
- 68: GND

Additional connections shown include:

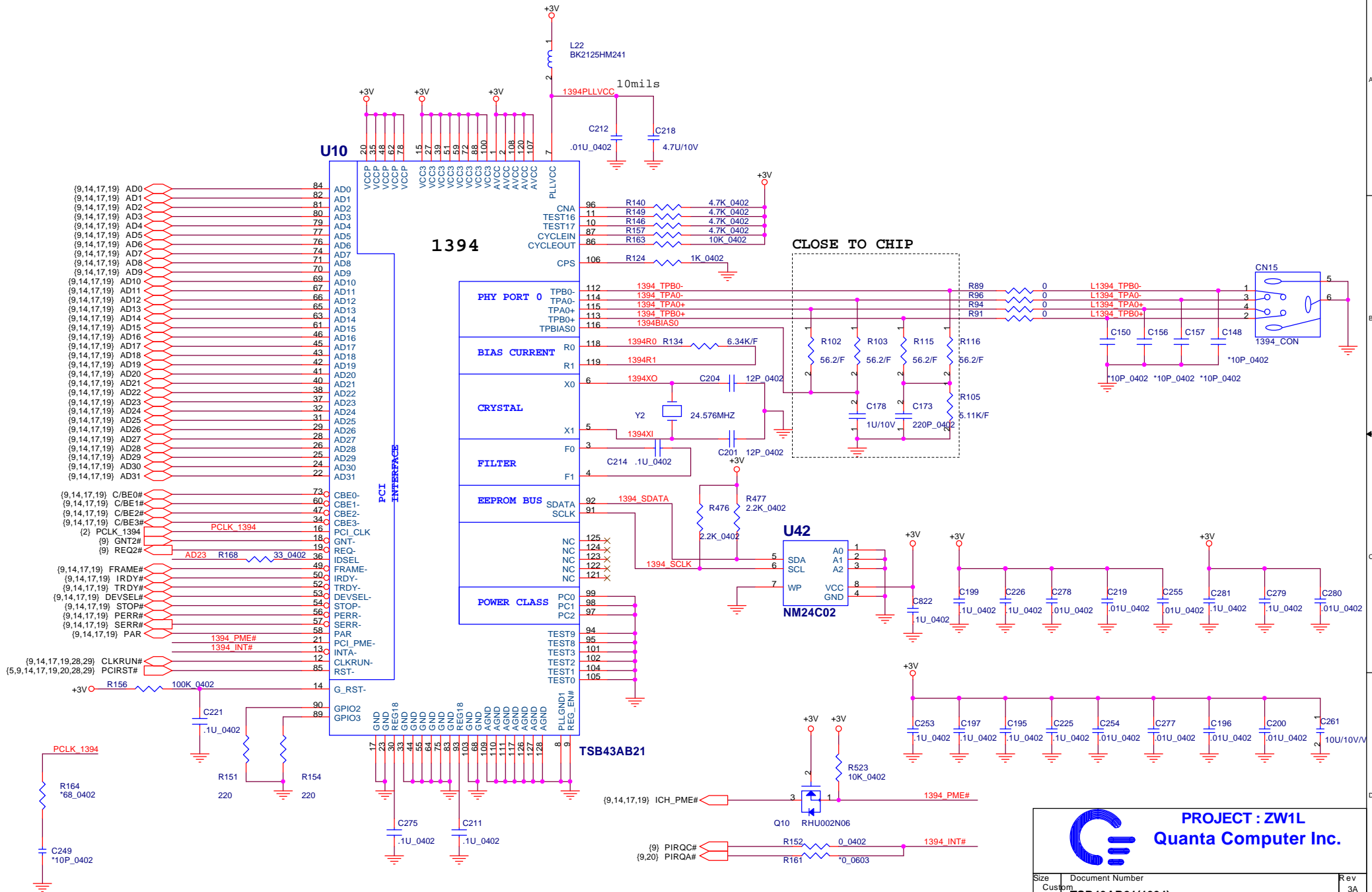
- A_VCC (Pin 17)
- A_VPP (Pin 18)
- A_CCLK (Pin 19)
- A_CIRDY (Pin 20)
- A_CC/BE2 (Pin 21)
- A_CAD18 (Pin 22)
- A_CAD20 (Pin 23)
- A_CAD21 (Pin 24)
- A_CAD22 (Pin 25)
- A_CAD23 (Pin 26)
- A_CAD24 (Pin 27)
- A_CAD25 (Pin 28)
- A_CAD26 (Pin 29)
- A_CAD27 (Pin 30)
- A_CAD29 (Pin 31)
- A_RSVD/D2 (Pin 32)
- A_CCLKRUN (Pin 33)
- A_CCD1 (Pin 36)
- A_CAD2 (Pin 37)
- A_CAD4 (Pin 38)
- A_CAD6 (Pin 39)
- A_RSVD/D14 (Pin 40)
- A_CAD8 (Pin 41)
- A_CAD10 (Pin 42)
- A_CVS1 (Pin 43)
- A_CAD13 (Pin 44)
- A_CAD15 (Pin 45)
- A_CAD16 (Pin 46)
- A_RSVD/A18 (Pin 47)
- A_CBLOCK (Pin 48)
- A_CSTOP (Pin 49)
- A_CDEVSEL (Pin 50)
- A_VCC (Pin 51)
- A_VPP (Pin 52)
- A_CTRDY (Pin 53)
- A_CFRAME (Pin 54)
- A_CAD17 (Pin 55)
- A_CAD19 (Pin 56)
- A_CVS2 (Pin 57)
- A_CRST (Pin 58)
- A_CSERR (Pin 59)
- A_CREQ (Pin 60)
- A_CC/BE3 (Pin 61)
- A_CAUDIO (Pin 62)
- A_CSTSCHNG (Pin 63)
- A_CAD28 (Pin 64)
- A_CAD30 (Pin 65)
- A_CAD31 (Pin 66)
- A_CCD2 (Pin 67)

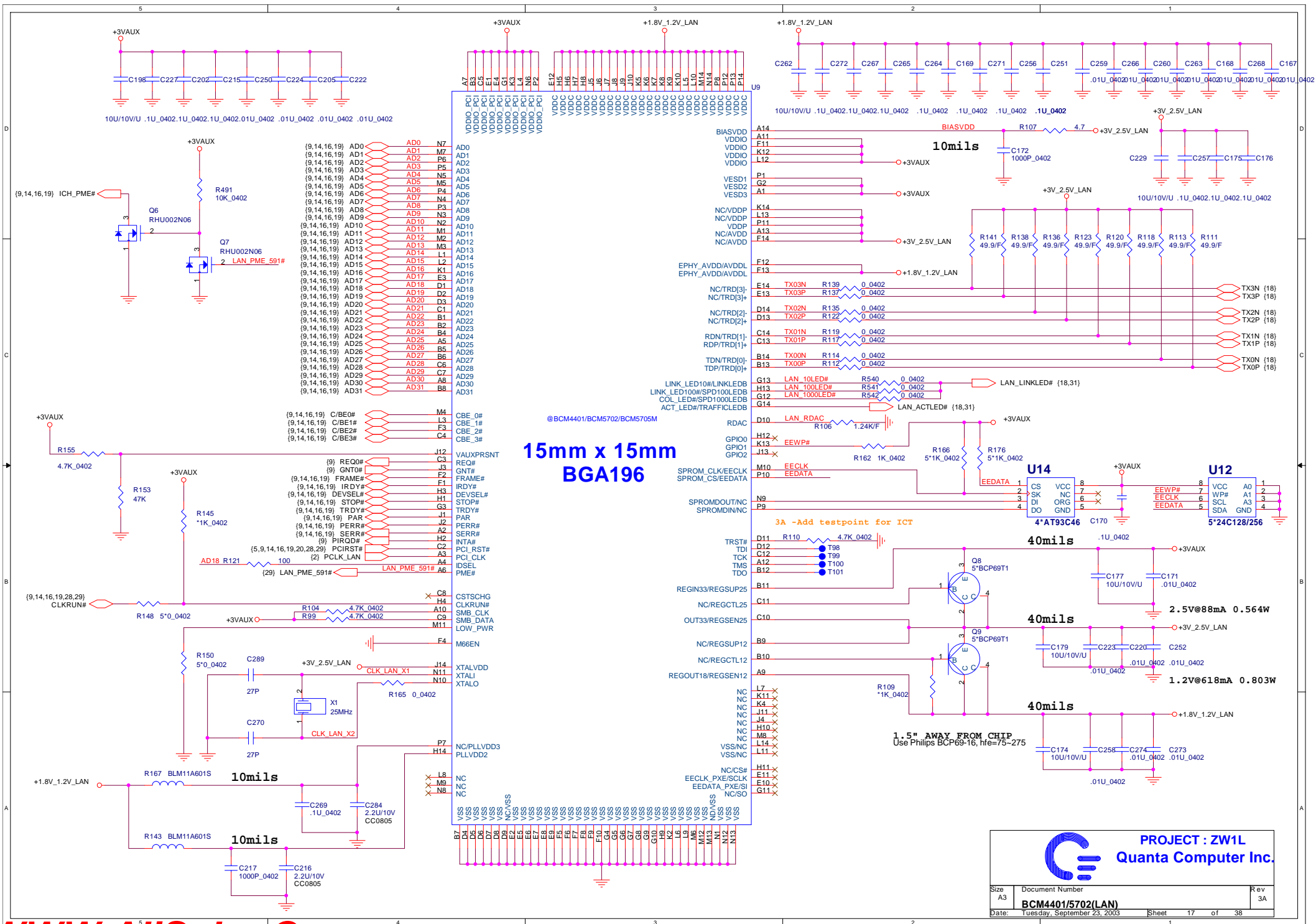
3A -CHANGE CARDBUS CON

CARDBUS PCMCIA SOCKET

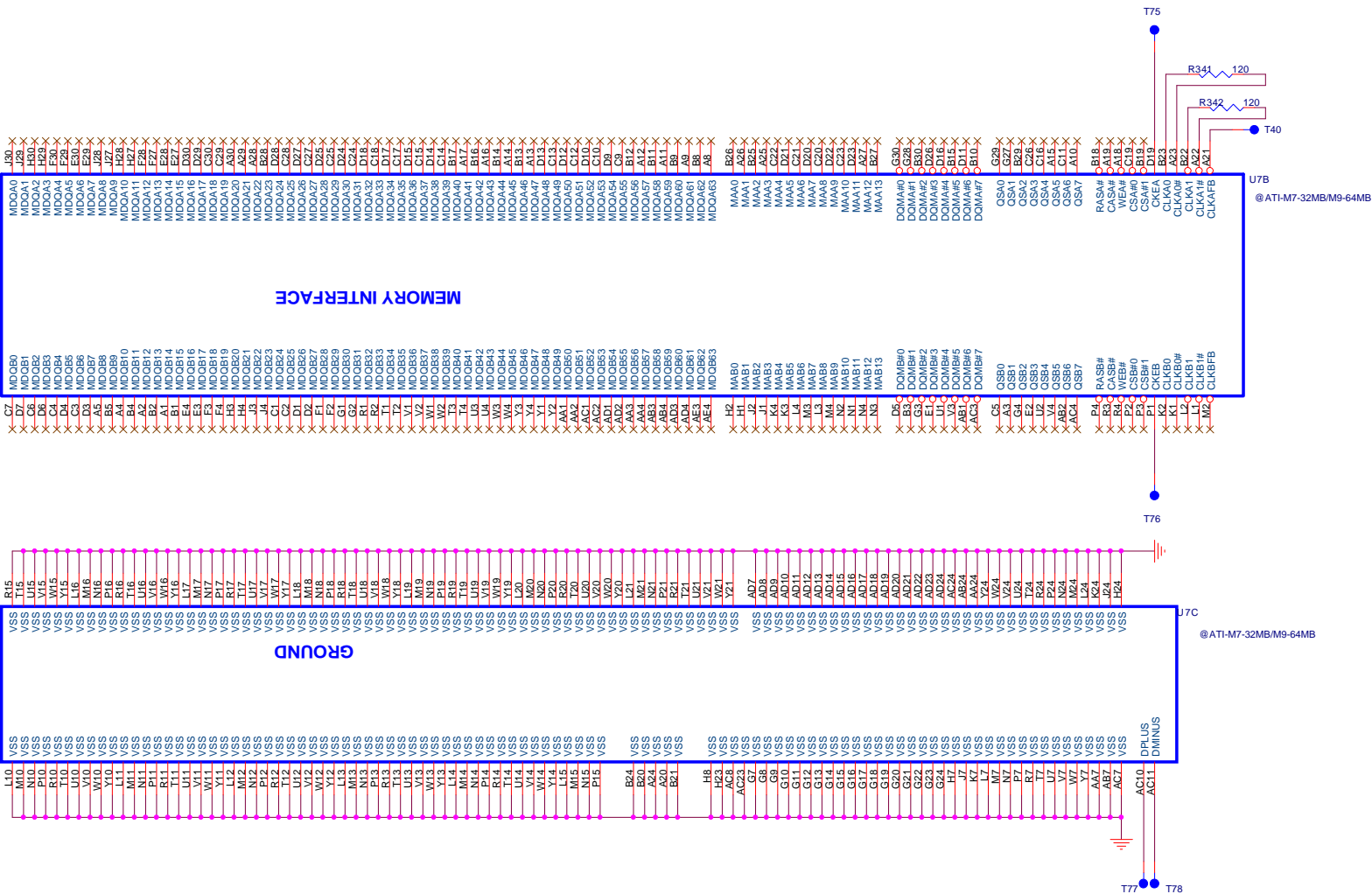


IEEE 1394 CONTROLLER - TI_TSB43AB21







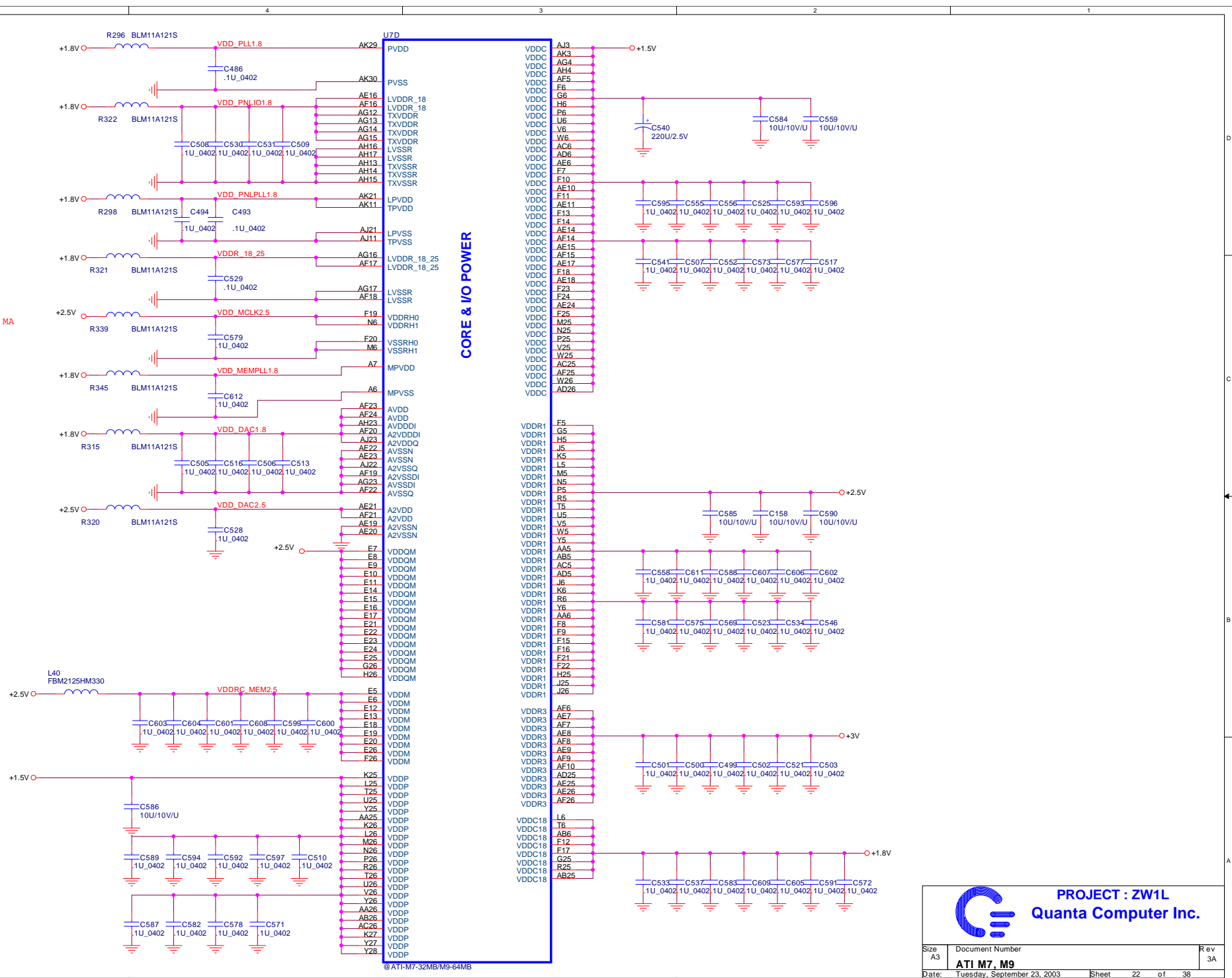


For The D7, the internal memory is on the A channel. Do not populate Termination or momory on this channel if you are making a D7 Board or Board variant.




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CORE & I/O POWER

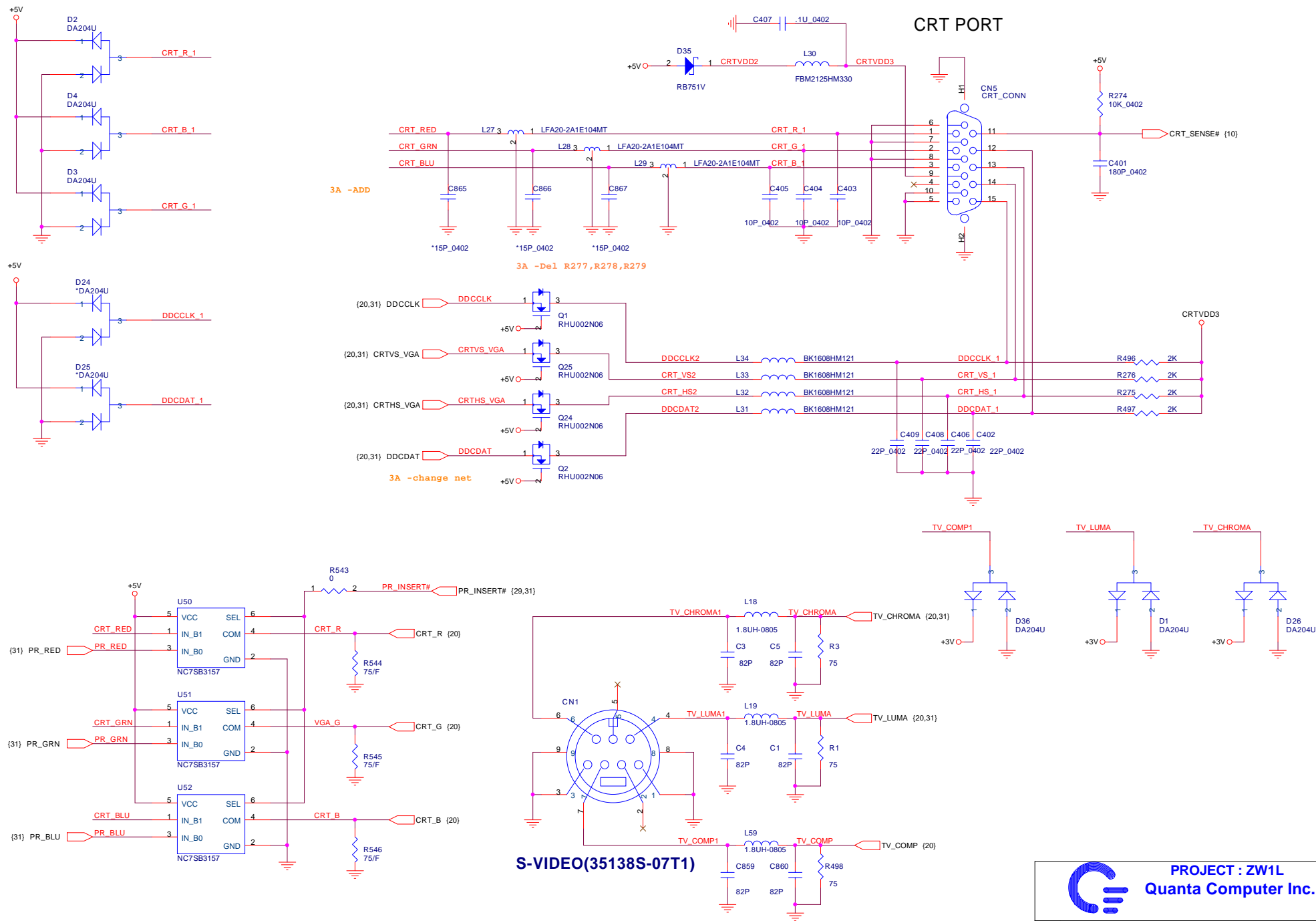
VDDRH 5 MA

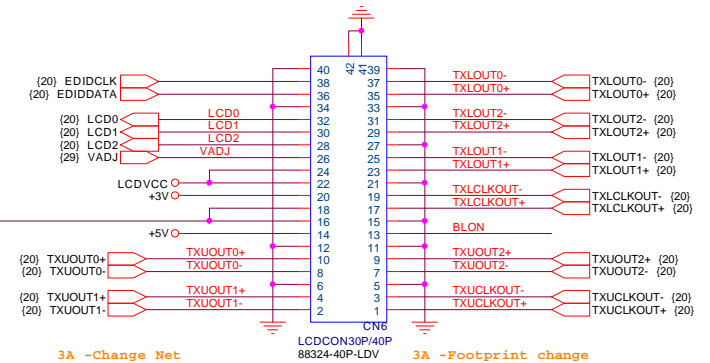
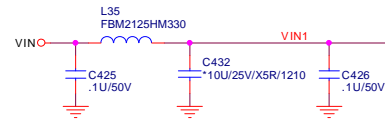
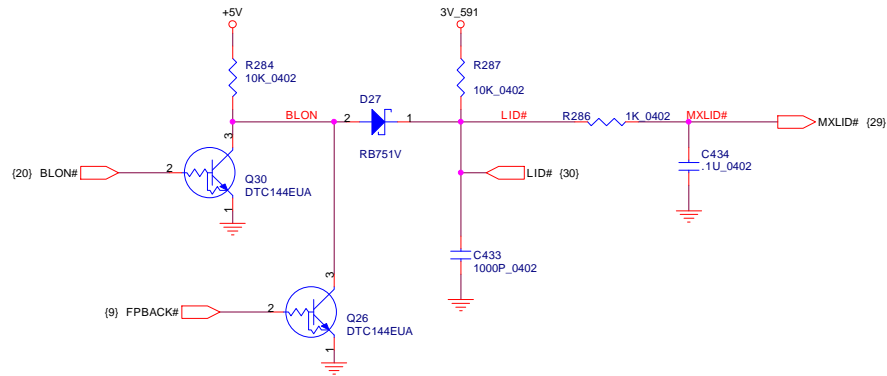
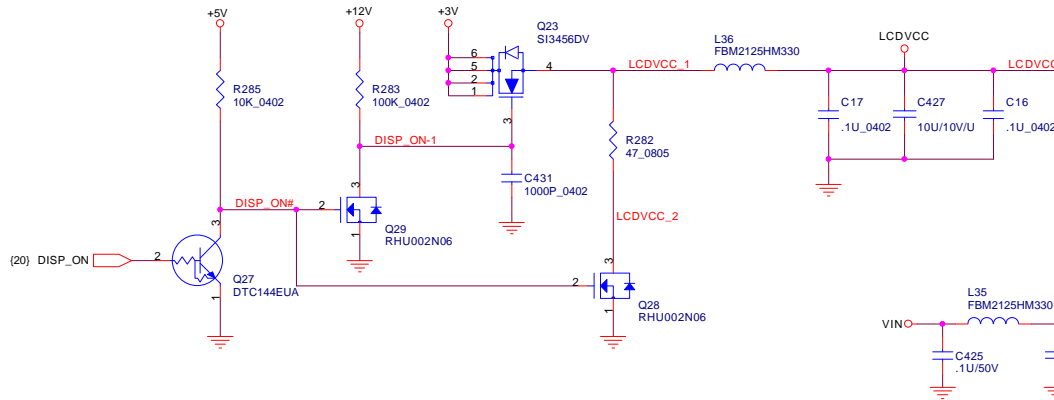


PROJECT : ZW1L

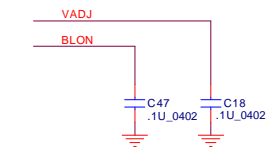
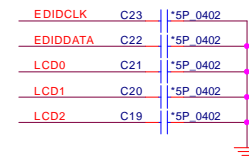
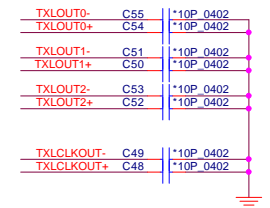
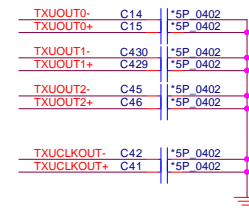
Quanta Computer Inc.

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ID_HEX	LCDTYPE	LCD2	LCD1	LCD0
0		0	0	0
1		0	0	1
2		0	1	1
3	XGA	0	1	1
4	UXGA	1	0	0
5	SXGA+	1	0	1
6	SXGA	1	1	0
7		1	1	1



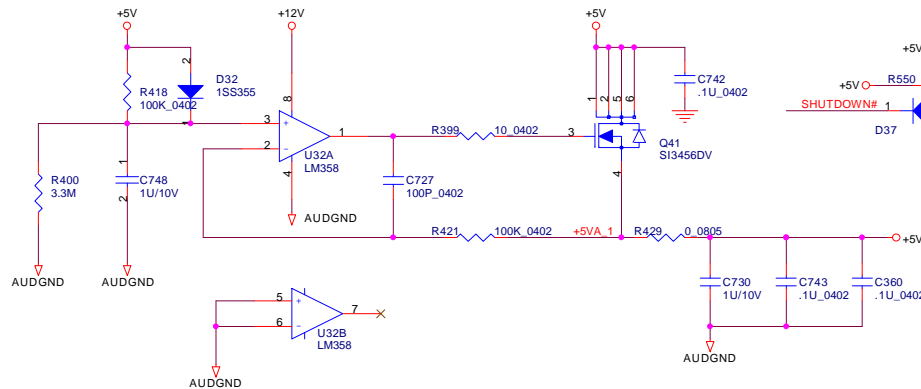
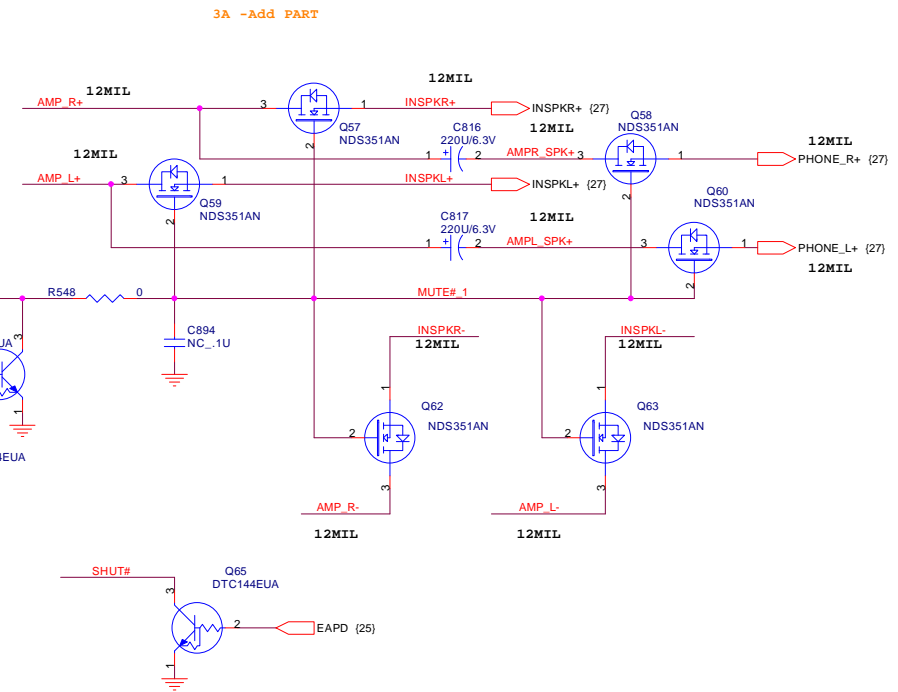
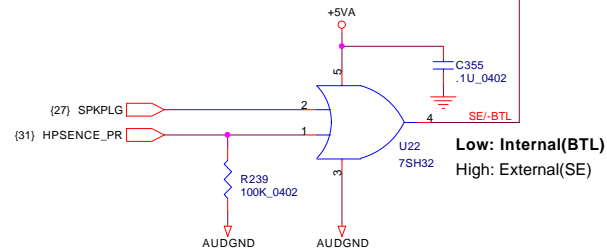
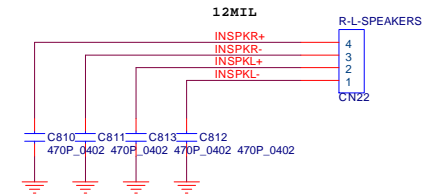
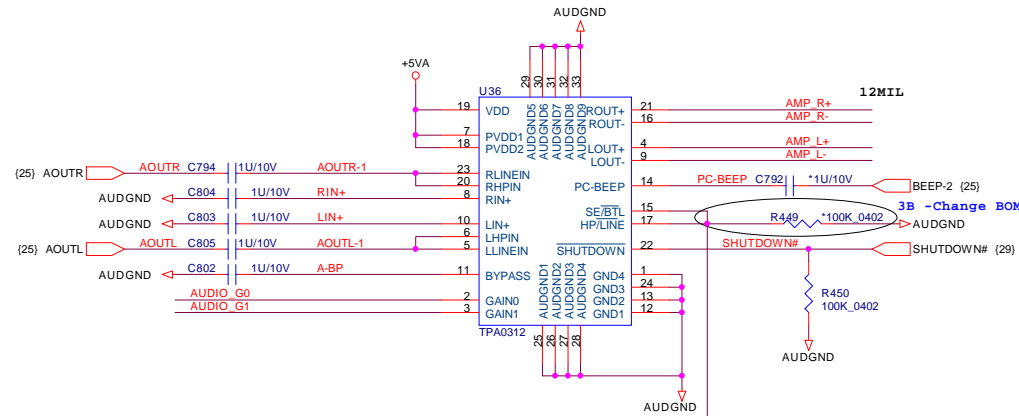
ID1	QDI	14.1" XGA	QD141X1LH12	0	0	1
ID2	LG	14.1" XGA	LP141X13	0	1	0
ID3	LG	15" XGA	LP150X05-A2C1	0	1	1
ID4	CPT	15" XGA	CLAA150XH01	1	0	0
ID5	CPT	15" SXGA+	CLAA150PA01	1	0	1
ID6	HannStar	15" SXGA+	HSD150PK12	1	1	0
ID7				1	1	1

PROJECT : ZW1L
Quanta Computer Inc.

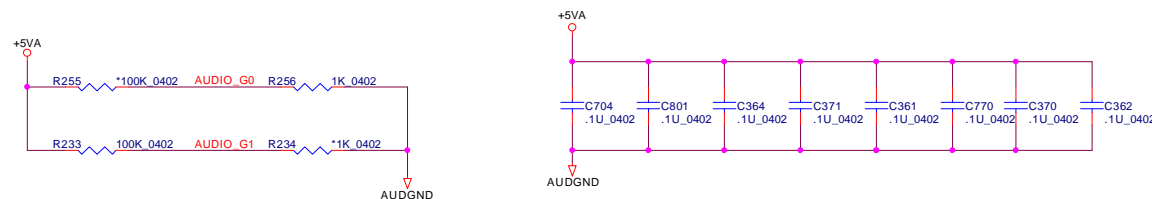
Size A3 Document Number LCD CONN & LCDVCC
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Audio Amplifier



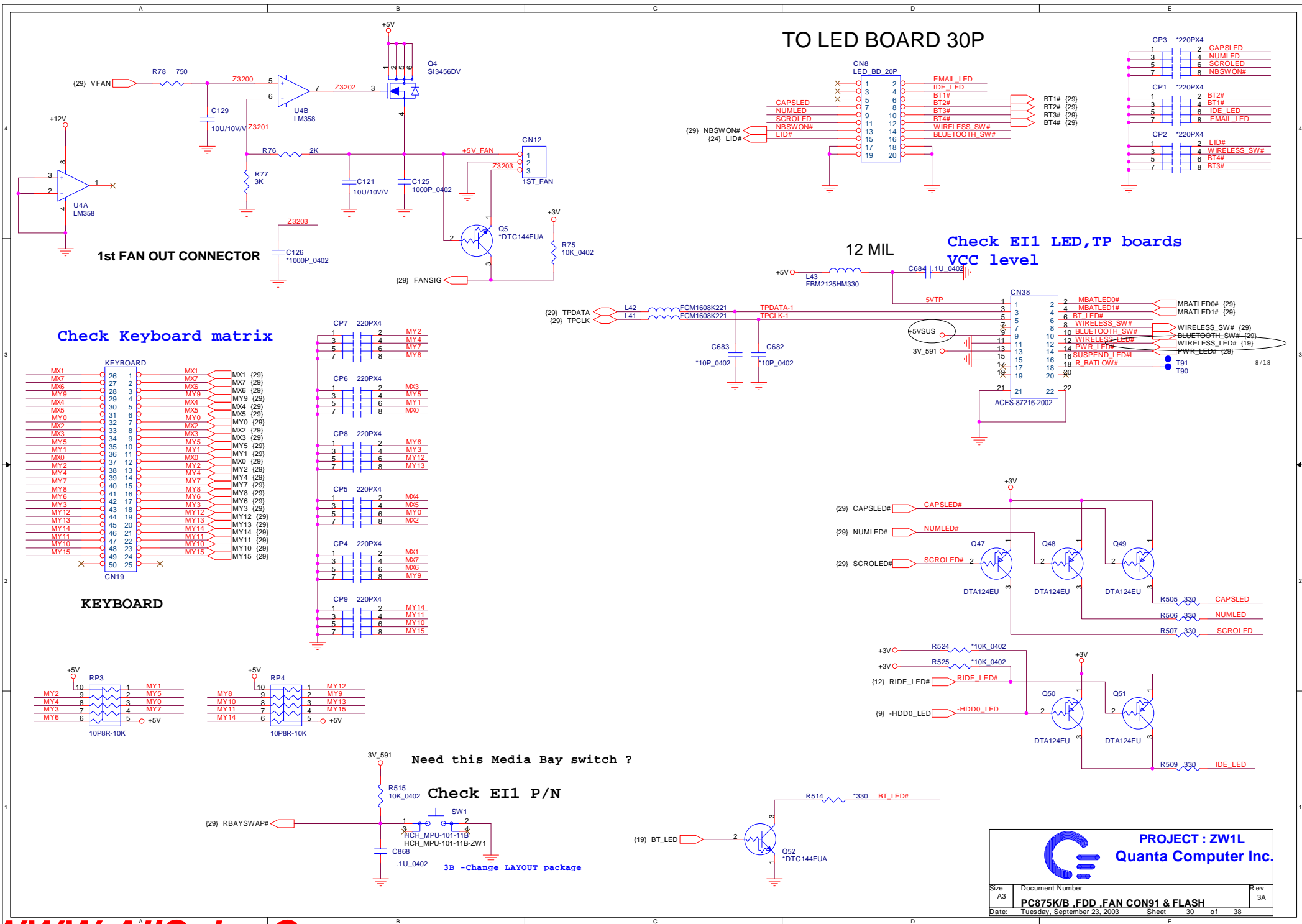
GAIN0	GAIN1	AV
0	0	6dB
0	1	10dB
1	0	15.6dB
1	1	21.6dB

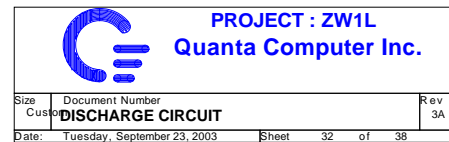


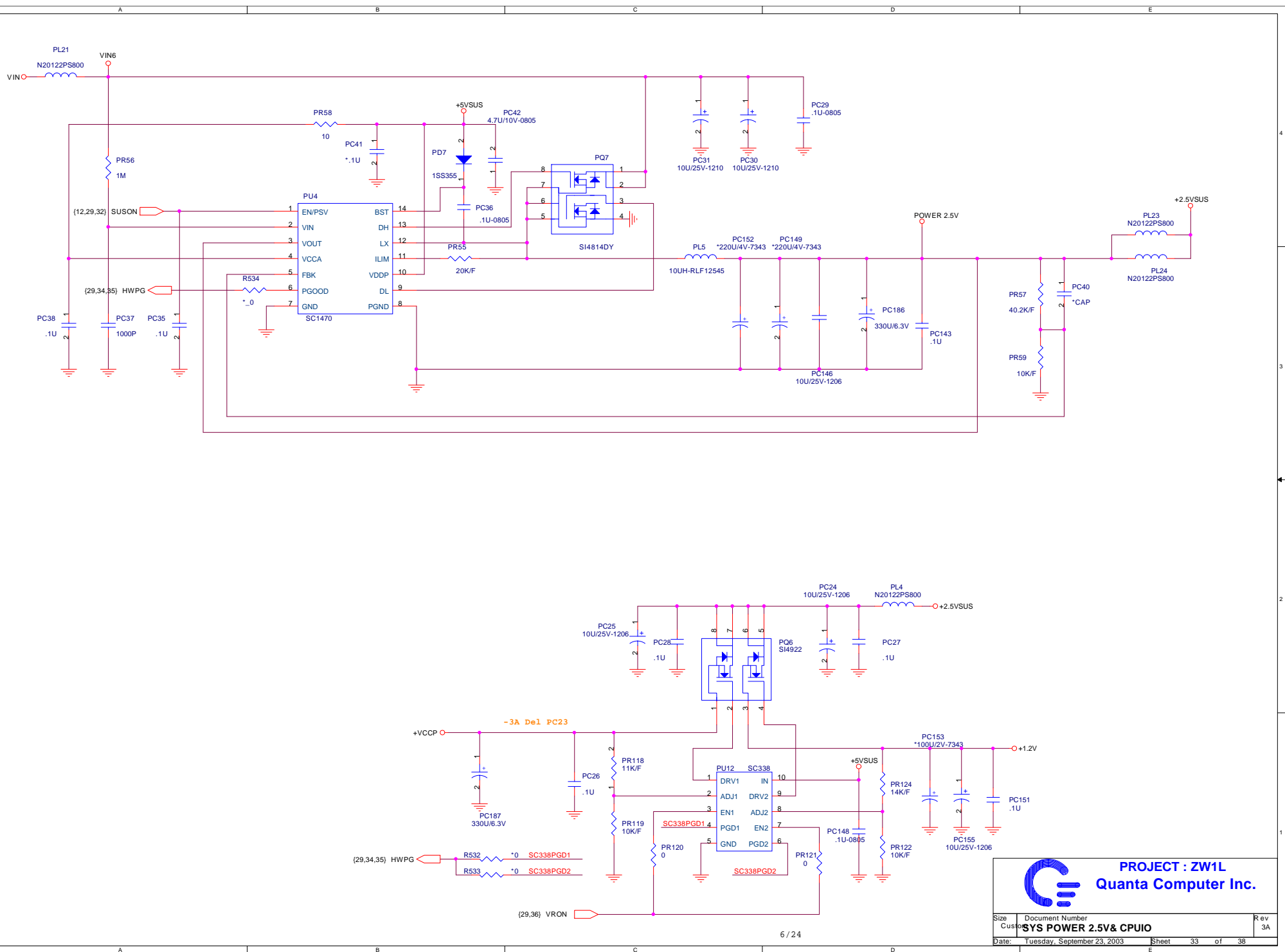
PROJECT : ZW1L
Quanta Computer Inc.


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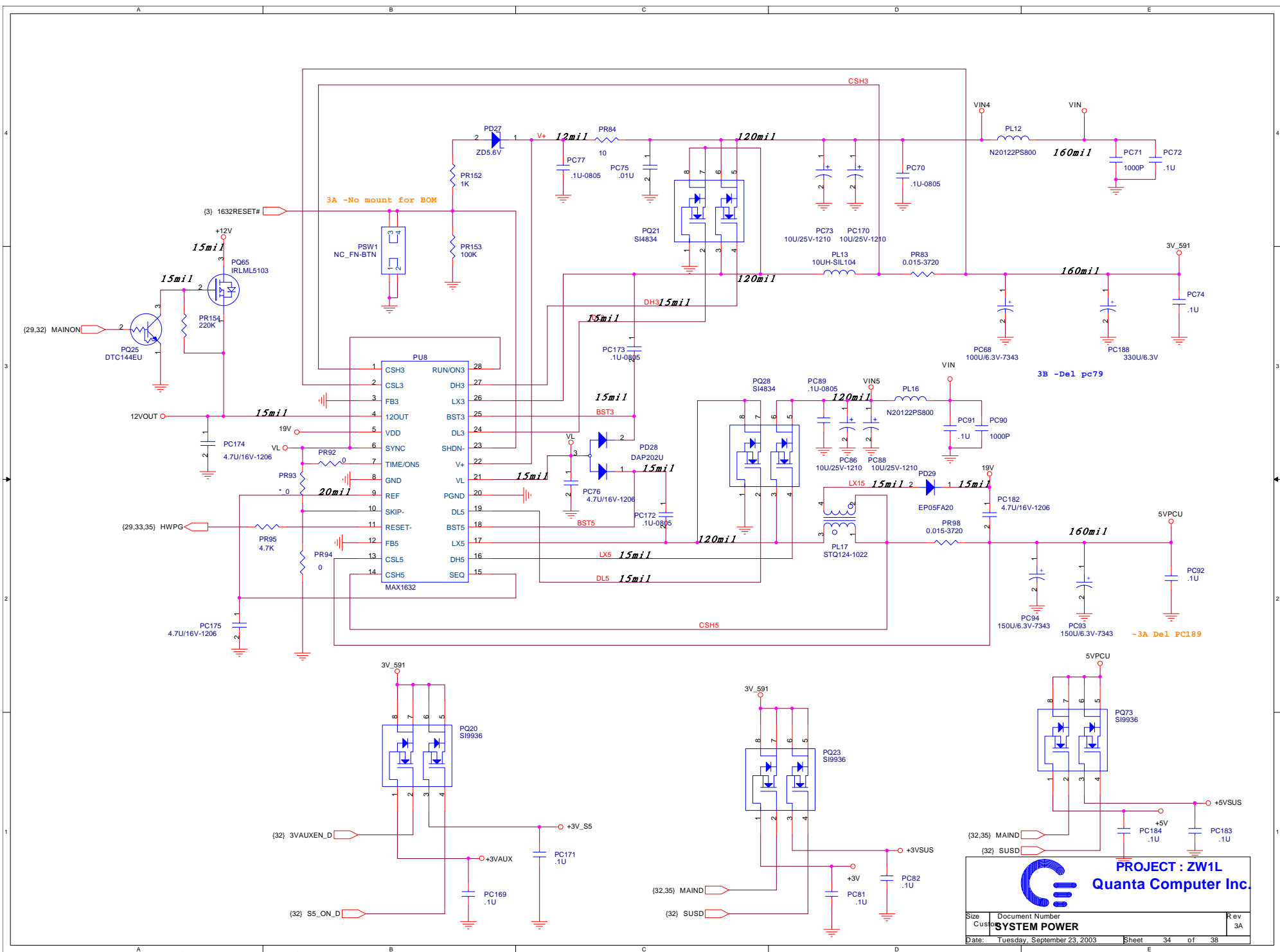


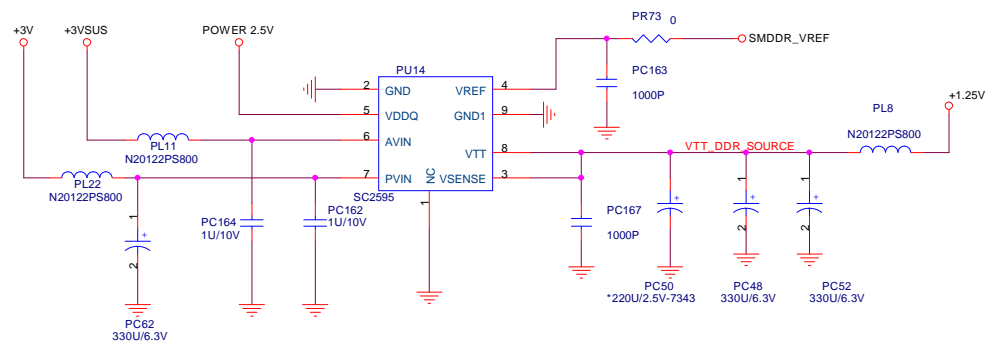
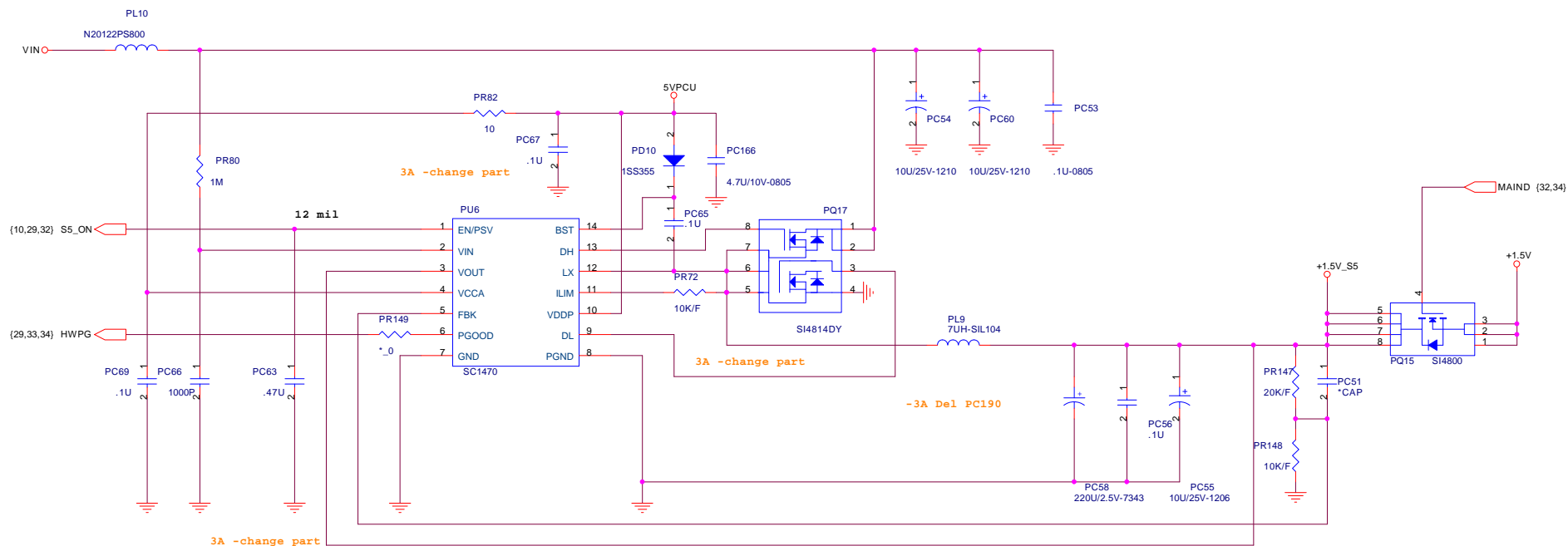



PROJECT : ZW1L

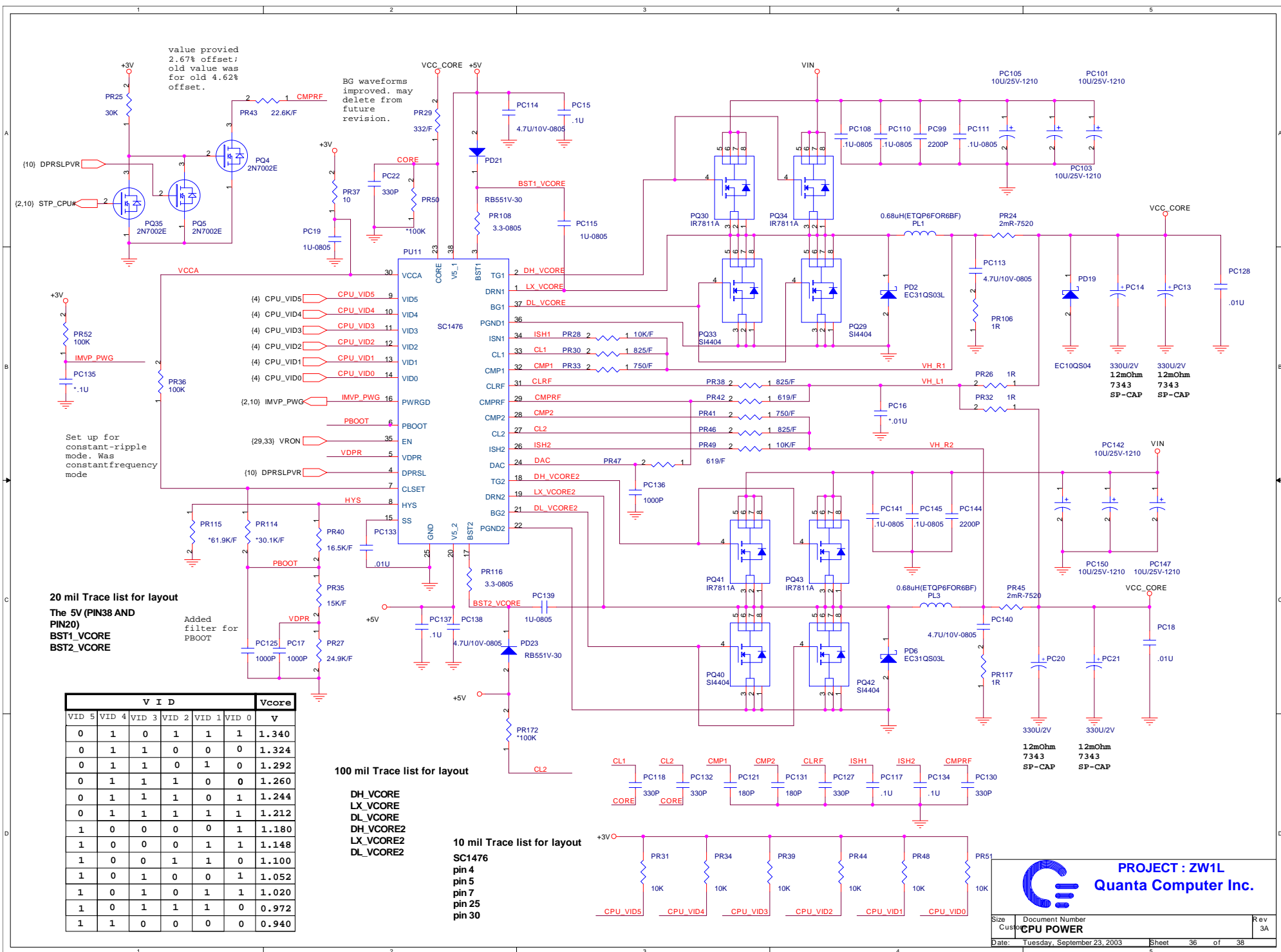
Quanta Computer Inc.

Size	Document Number	Rev
Customer	SYS POWER 2.5V& CPUIO	3A
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		Quanta Computer Inc.	
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Quanta Computer Inc.

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Cust: CPU POWER
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